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NOTICE OF ALLOWANCE AND FEE(S) DUE

21839 7590 0226/2009 BUCHANAN, INGERSOLL & ROONEY PC POST OFFICE BOX 1404 ALEXANDRIA, VA 22313-1404

EXAMINER				
N, SEAN P				
PAPER NUMBER				
)				

	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
10/587,574 07/31/2006			Janne Kuivalainen	1034456-000049	8935			
	TITLE OF INVENTION, THERMAL OVERLOAD PROTECTION							

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	05/26/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT, PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

INSTRUCTIONS: This f appropriate. All further c indicated unless corrected maintenance fee notificati	form should be used f orrespondence includin d below or directed oth ons.	or tran g the erwise	nsmitting the ISSU Patent, advance on in Block 1, by (a					hould be completed where correspondence address as trate "FEE ADDRESS" for
CURRENT CORRESPONDE	NCE ADDRESS (Note: Use Blo	ock I for	any change of address)	pap	ers. Each additiona	d paper	g can only be used fo licate cannot be used f , such as an assignme iling or transmission.	r domestic mailings of the or any other accompanying nt or formal drawing, must
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								(Signature)
								(Date)
APPLICATION NO.	FILING DATE			FIRST NAMED INVENTOR		ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/587,574	07/31/2006		·	Janne Kuivalainen		1	034456-000049	8935
TITLE OF INVENTION:								1
APPLN, TYPE	SMALL ENTITY	IS	SUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO		\$1510	\$300	\$0		\$1810	05/26/2009
EXAMI	NER		ART UNIT	CLASS-SUBCLASS				
SHECHTMA	N, SEAN P		2121	700-299000				
"Fee Address" indic PTO/SB/47; Rev 03-02 Number is required. 3. ASSIGNEE NAME AN	ndence address (or Cha 7122) attached. cation (or "Fee Address" c or more recent) attach TO RESIDENCE DATA sss an assignce is identi in 37 CFR 3.11. Comp	nge of 'Indica ed. Usa	Correspondence ation form e of a Customer		o 3 registered pater vely, le firm (having as a agent) and the nam rmeys or agents. If printed. pe)	memb es of u no nan	er a 2p to p to be is 3	ocument has been filed for
Please check the appropria	ate assignee category or	catego	ories (will not be pr	inted on the patent):	Individual 🚨 Co	orporat	ion or other private gro	oup entity Government
4a. The following fee(s) are submitted: Issue Fee Publication Fee (No small entity discount permitted) Advance Order - # of Copies Advance Order - # of Copies The Discount permitted Advance Order - # of Copies The Discount permitted The						ficiency, or credit any		
	SMALL ENTITY statu	s. See	37 CFR 1.27.	b. Applicant is no lor				
NOTE: The Issue Fee and interest as shown by the re	Publication Fee (if requeecords of the United Sta	iired) v tes Pat	will not be accepted ent and Trademark	from anyone other than Office.	the applicant; a regi	stered	attorney or agent; or th	ne assignee or other party in
Authorized Signature _					Date			
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This collection of informa an application. Confidenti submitting the completed this form and/or suggestio Box 1450, Alexandria, Vir Alexandria, Virginia 2231	tion is required by 37 C ality is governed by 35 application form to the ons for reducing this bur rginia 22313-1450. DC 3-1450.	FR 1.3 U.S.C. USPT den, sl NOT	11. The informatic . 122 and 37 CFR O. Time will vary hould be sent to th SEND FEES OR 0	on is required to obtain or 1.14. This collection is es depending upon the indi e Chief Information Offic COMPLETED FORMS T	retain a benefit by t timated to take 12 i vidual case. Any co er, U.S. Patent and O THIS ADDRESS	he pub minute ommen Trader S. SEN	lic which is to file (and s to complete, includin ts on the amount of tin nark Office, U.S. Dep D TO: Commissioner	by the USPTO to process) g gathering, preparing, and me you require to complete artment of Commerce, P.O. for Patents, P.O. Box 1450,

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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS

P O Box 1450 Alexandria, Virgima 22313-1450 www.uspto.gov

DATE MAILED: 02/26/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/587,574 07/31/2006 Janne Kuiv		Janne Kuivalainen	1034456-000049	8935
21839	590 02/26/2009	EXAMINER		
BUCHANAN, I	NGERSOLL & ROO	SHECHTMA	AN, SEAN P	
POST OFFICE B		ART UNIT	PAPER NUMBER	
ALEXANDRIA,	VA 22313-1404	2121		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability

Application No.	Applicant(s)	
10/587,574	KUIVALAINEN ET A	L.
Examiner	Art Unit	
Sean P. Shechtman	2121	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address-All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included
herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS
NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative
of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

- This communication is responsive to 2/5/09.
- The allowed claim(s) is/are 1,3,6,7,11,14 and 19.
- 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a)

 All b)

 Some* c)

 None of the:
 - 1. A Certified copies of the priority documents have been received.
 - 2. Certified copies of the priority documents have been received in Application No. _____
 - Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

- 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
- 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) 🔲 including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date _____
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying Indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

 DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- 1. Notice of References Cited (PTO-892)
- 2. Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3. Information Disclosure Statements (PTO/SB/08),
- Paper No./Mail Date ____
- Examiner's Comment Regarding Requirement for Deposit of Biological Material
- 5. Notice of Informal Patent Application
- Interview Summary (PTO-413), Paper No./Mail Date .
- 7. Examiner's Amendment/Comment
- 8. X Examiner's Statement of Reasons for Allowance
- 9. 🔲 Other ____

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. Rejections withdrawn.

Claim Rejections - 35 USC § 103

Rejections withdrawn.

Allowable Subject Matter

3. Claims 1, 3, 6, 7, 11, 14, 19, are allowed.

The following is an examiner's statement of reasons for allowance:

While Libert teaches device/method/processor and memory storing instructions for thermal overload protection of an electrical device, particularly an electric motor (Abstract; Col. 1, lines 46-64), comprising a current meter configured to measure at least one load current supplied to the electrical device (Col. 33, lines 50-51), a processor system configured to calculate a thermal load on the electrical device on the basis of said at least one load current, and a switch device disconnecting a current supply when the thermal load reaches a given threshold level, said processor system employing X-bit, and being configured to scale the measured current into unit values to a range of 0 to Y, wherein Y represents Y/100% of a nominal current and Y is a real number greater than zero (Col. 11, lines 24-56), and to calculate the thermal load using a mathematical equation that, together with its operands, is programmed into the processor system structured (Col. 33, line 52 – Col. 34, line 5; Col. 22, lines 29 – Col. 23, line 49).

And, Chan teaches a processor system employing 32-bit fixed-point arithmetic, and the processor system structured such that calculating a result or a provisional result never exceeds the 32-bit value (paragraph 30 – paragraph 58, especially paragraph 51). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 32 in the case of the 32-bit DSP.

And, Onaga teaches a processor system employing X-bit fixed-point arithmetic, and the processor system structured such that a result or a provisional result never exceeds the X-bit value (Col. 11, lines 1-5, TMS-32010 16bit processor). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 16 in the case of the TMS-32010.

And, Moscrip teaches a processor system employing 32-bit fixed-point arithmetic, structured such that a result or a provisional result never exceeds the 32-bit value (Col. 13, lines 36- Col. 14, line 9). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 32 in the case of the MC68332 or 32-bit fixed point DSPs.

Referring to claims 1, 7, 19, none of Libert, Chan, Onaga or Moscrip, taken either alone or in obvious combination discloses a method/device/processor and memory storing executable instructions having all the claimed features of applicant's instant invention, specifically including:

calculating the thermal load using a mathematical equation structured such that a result or a provisional result never exceeds the 32-bit value;

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wherein the mathematical equation is .THETA. k = .DELTA. .times. .times. T * i 2 C + (1 - .DELTA. .times. .times. T R * C) * .THETA. k – 1;

wherein .THETA.=currently calculated thermal load, wherein .THETA. k1=previous thermal load, .DELTA.T=interval for thermal load calculation, R=cooling factor of electrical device, C=trip-class factor, i= measured current.

Claims 3, 6, 11, 14 depend from claims 1, 7, or 19 and are therefore also allowable. It is for these reasons that applicant's invention defines over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

 The prior art or art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents or publications are cited to further show the state of the art with respect to a device and method for thermal overload protection of an electrical device, particularly an electric motor (M), the device comprising means for measuring at least one load current supplied to the electrical device (M), means for calculating the thermal load on the electrical device on the basis of said at least one load current, and means (S2) for interrupting a current supply (L1, L2, L3) when the thermal load reaches a given threshold level, wherein the device comprises a processor system employing X-

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bit, preferably X=32, fixed-point arithmetic, the system comprising means for scaling the current measured into unit values to a range of 0 to Y, wherein Y represents Y/100% of a nominal current, and means for calculating a time-to-trip in accordance with formula, whereby the equation and its operands are programmed into the microprocessor system structured such that a result or a provisional result never exceeds the X-bit value.

U.S. Pub. No. 2008/0253041 to Kuivalainen.

 Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sean P. Shechtman whose telephone number is (571)272-3754. The examiner can normally be reached on 9:30am-6:00pm, M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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SPS Sean P. Shechtman February 14th, 2009

/Sean P. Shechtman/ Primary Examiner, Art Unit 2121